



PATENTS
ALT-323 Cont. Div.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Bonnie I. Wang et al.
Application No. : 10/702,300 Confirmation No.: 7980
Filed : November 5, 2003
For : PROGRAMMABLE PHASE SHIFT CIRCUITRY

New York, New York 10020
March 29, 2004

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97,
applicants hereby make the following patents and
publications of record in the above-identified patent
application:

JenningsCheck U.S. Patent 5,059,924 (October 22,
1991)
Wahler et al. U.S. Patent 5,170,297 (December 8,
1992)
Brown U.S. Patent 5,185,581 (February 9, 1993)
Kaplinsky U.S. Patent 5,298,866 (March 29, 1994)
Torode U.S. Patent 5,451,912 (September 19, 1995)
Maitland et al. U.S. Patent 5,561,692 (October 1,
1996)
Sutardja et al. U.S. Patent 5,635,879 (June 3,
1997)
Jefferson U.S. Patent 5,642,082 (June 24, 1997)
Jefferson U.S. Patent 5,699,020 (December 16,
1997)

Jefferson U.S. Patent 5,699,020 (December 16,
1997)
Rao U.S. Patent 5,701,270 (December 23, 1997)
Sauer U.S. Patent 5,731,743 (March 24, 1998)
Jefferson et al. U.S. Patent 5,744,991 (April 28,
1998)
Yeung et al. U.S. Patent 5,889,436 (March 30,
1999)
Dell et al. U.S. Patent 5,896,346 (April 20,
1999)
Jefferson et al. U.S. Patent 5,963,069
(October 5, 1999)
Li U.S. Patent 5,970,110 (October 19, 1999)
Lu U.S. Patent 6,100,735 (August 08, 2000)
Huang et al. U.S. Patent 6,114,915 (September 5,
2000)
Jefferson U.S. Patent 6,326,812 (December 4,
2001)
Mair et al. U.S. Patent 6,329,850 (December 11,
2001)

Wolaver, Dan H., Phase-Locked Loop Circuit
Design, PTR Prentice Hall, Englewood Cliffs, New
Jersey, 1991, pp. 68-70

"ORCA[®] OR3Cxx (5 V) and OR3Txxx (3.3 V) Series
Field-Programmable Gate Arrays," Lucent Technologies
Microelectronics Group, Preliminary Data Sheet,
November 1997

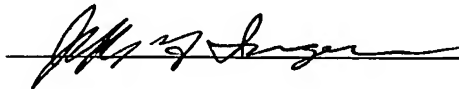
The aforementioned patent documents, which are
listed on the accompanying Form PTO-1449 (submitted in
duplicate), were cited by either applicants or the Examiner
in parent Application No. 10/056,787, filed January 23,
2002 or in grandparent Application No. 09/432,142, filed
November 2, 1999. Accordingly, pursuant to 37 C.F.R.
§ 1.98(d), no copies are enclosed.

It is respectfully requested that these patents
and publications be: (1) fully considered by the Patent and
Trademark Office during the examination of this
application; and (2) printed on any patent which may issue

on this application. Applicants request that a copy of Form PTO-1449, as considered and initialed by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

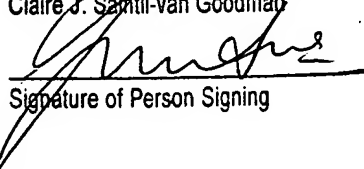
Respectfully submitted,



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March 29, 2004
Claire J. Santil-van Goodman

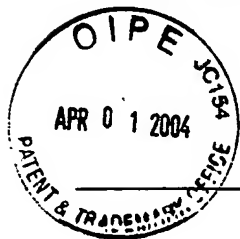


Signature of Person Signing

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
ALT-323 Cont. Div.APPLICATION NO.
10/702,300INFORMATION DISCLOSURE
STATEMENT BY APPLICANTSAPPLICANTS
Bonnie I. Wang et al.CONFIRMATION NO.
7980FILING DATE
November 5, 2003

GROUP ART UNIT



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,059,924	10/22/91	JenningsCheck	331	25	
	5,170,297	12/08/92	Wahler et al.	360	51	
	5,185,581	02/09/93	Brown	331	108B	
	5,298,866	03/29/94	Kaplinsky	328	155	
	5,451,912	09/19/95	Torode	331	1A	
	5,561,692	10/01/96	Maitland et al.	327	153	
	5,635,879	06/03/97	Sutardja et al.	331	135	
	5,642,082	06/24/97	Jefferson	331	25	
	5,699,020	12/16/97	Jefferson	331	17	
	5,701,270	12/23/97	Rao	365	230.03	
	5,731,743	03/24/98	Sauer	331	57	
	5,744,991	04/28/98	Jefferson et al.	327	158	
	5,889,436	03/30/99	Yeung et al.	331	25	
	5,896,346	04/20/99	Dell et al.	365	233	
	5,963,069	10/05/99	Jefferson et al.	327	158	
	5,970,110	10/19/99	Li	327	147	
	6,100,735	08/08/00	Lu	327	158	
	6,114,915	09/05/00	Huang et al.	331	25	
	6,326,812	12/04/01	Jefferson	326	93	
	6,329,850	12/11/01	Mair et al.	327	156	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANTS	ATTY. DOCKET NO. ALT-323 Cont. Div.	APPLICATION NO. 10/702,300
	APPLICANTS Bonnie I. Wang et al.	CONFIRMATION NO. 7980
	FILING DATE November 5, 2003	GROUP ART UNIT

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
	Wolaver, Dan H., <i>Phase-Locked Loop Circuit Design</i> , PTR Prentice Hall, Englewood Cliffs, New Jersey, 1991, pp. 68-70
	"ORCA® OR3Cxx (5 V) and OR3Txxx (3.3 V) Series Field-Programmable Gate Arrays," Lucent Technologies Microelectronics Group, Preliminary Data Sheet, November 1997

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